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**CERTIFICATE OF MAILING**  
**37 § C.F.R. 1.8**

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11-15-05  
Date

Stephanie Davis  
Stephanie Davis

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

John Harper, et al.

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Group Art Unit: 2676

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Examiner: unk

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Examiner phone: unk

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For: **Methods for Dynamically Combining  
GPU Fragment Programs**

§

Atty. Dkt. No.: P3352US1 (119-0033US)

§

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria VA, 22313-1450.

Sir:

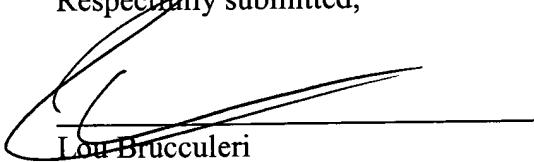
In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. In accordance with this Office's waiver of the provision of 37 CFR 1.98 (a)(2)(ii) for submitting copies of U.S. patents, no copies of these references are included. Copies will however be submitted upon request.

The present Supplemental Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in accordance with 37 C.F.R § 1.97(b). No fees are believed to be due in connection with the filing of this Supplemental Information Disclosure Statement. However, the Commissioner is authorized to deduct any necessary fees from Deposit Account No. 501922, referencing matter no. 119-0033US.

Applicants respectfully request that the listed documents be considered and made of record in the present case, and that the Examiner initial the appropriate spaces on the Form 1449 to evidence the same

11/15/05  
Date

Respectfully submitted,

  
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NOV 18 2005

## List of Patents and Publications for Applicant's

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Sheet

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of

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Atty. Docket No. P3352US1 (119-0033US)	Application No. 10/826,762
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First Named Inventor/Applicant: John Harper, et al. / Apple Computer, Inc.
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Examiner Name: Unknown
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Filing Date: April 16, 2004	Group Art Unit: 2676
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### U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub. Class	Filing Date of App.
	A1	6,717,599	04/06/2004	Olano	345	853	06/29/2000
	A2	2003/174136-A1	09/18/2003	Emberling et al.			
	A3	2002/118217-A1	08/29/2002	Fujiki			
	A4	6,272,558	08/07/2001	Hui et al.	709	328	10/06/1997
	A5	6,006,231	12/21/1999	Popa	707	101	09/10/1997
	A6	5,490,246	02/06/1996	Brotsky et al.	395	161	08/23/1994

### Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1	EP 1 383 080	01/21/2004	EP			
	B2	WO 02/09039A	01/31/2002	WO			
	B3	EP 0 694 879	01/31/1996	EP			
	B4	EP 548 586 A	06/30/1993	EP			

### Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
	C1	International Search report dated July 27, 2005 (PCT/US 05/008804; 119-0033WO)
	C2	International Search report dated August 8, 2005 (PCT/US 05/008805; 119-0034WO)
	C3	Hauberli, P. et al., "The Accumulation Buffer: Hardware Support for High-Quality Rendering," Computer Graphics, New York, NY, vol. 24, No. 4, Aug. 1, 1990, pgs. 309-318.

**EXAMINER:****DATE CONSIDERED:**

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.